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L Number	Hits	Search Text	DB	Time stamp
-	754	348/311	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:10
-	492	348/312	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:10
-	1019	348/311 or 348/312	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:10
-	824	(348/311 or 348/312) and ccd	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:19
-	28	((348/311 or 348/312) and ccd) and buses	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:11
-	7	((348/311 or 348/312) and ccd) and buses) and capacitors	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:14
-	80	ccd and busses and capacitors	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:15
-	172	ccd and buses and capacitors	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:14
-	233	(ccd and busses and capacitors) or (ccd and buses and capacitors)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:15
-	36	((ccd and busses and capacitors) or (ccd and buses and capacitors)) and clock adj driver	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:22
-	0	(348/311 or 348/312) and ccd with capatitor\$2 with Bus\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:20
-	0	(348/311 or 348/312) and ccd with capatitor\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:20
-	0	ccd with capatitor\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:20
-	1055	ccd with capacitor\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:20

-	3	ccd with capacitor\$2 with bus\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT	2004/10/25 14:20
-	0	20030103155.URPN.		2004/10/25 14:22
-	169	clock adj driver with (ccd or image adj sensor or image-sensor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:23
-	47	(clock adj driver with (ccd or image adj sensor or image-sensor)) and capacitor\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:24
-	41	((clock adj driver with (ccd or image adj sensor or image-sensor)) and capacitor\$2) and switch\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:24
-	11	((((clock adj driver with (ccd or image adj sensor or image-sensor)) and capacitor\$2) and switch\$3) and bus\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:26
-	5	image adj sensor adj clock adj driver	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:28
-	1738783	image adj sensor near\$3 clock adj driver	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:28
-	13	image adj sensor near3 clock adj driver	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:47
-	19777	(image adj sensor or image-sensor or ccd) and clock	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:47
-	7777	((image adj sensor or image-sensor or ccd) and clock) and phase	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:47
-	2745	((image adj sensor or image-sensor or ccd) and clock) and phase with two	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:48
-	496	((((image adj sensor or image-sensor or ccd) and clock) and phase with two) and capacitors	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:48
-	379	(((((image adj sensor or image-sensor or ccd) and clock) and phase with two) and capacitors) and shift	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:48
-	275	(((((image adj sensor or image-sensor or ccd) and clock) and phase with two) and capacitors) and shift adj register\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:49

-	145	(((((image adj sensor or image-sensor or ccd) and clock) and phase with two) and capacitors) and shift adj register\$2) and (two-phase or two adj phase)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:49
-	57	(((((image adj sensor or image-sensor or ccd) and clock) and phase with two) and capacitors) and shift adj register\$2) and (two-phase or two adj phase) with shift adj register\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:49
-	711	(two-phase or two adj phase) with shift adj register\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:49
-	262	((two-phase or two adj phase) with shift adj register\$2) and ccd	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:50
-	96	((((two-phase or two adj phase) with shift adj register\$2) and ccd) and capacitor\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 14:50
-	72	(((((two-phase or two adj phase) with shift adj register\$2) and ccd) and capacitor\$2) and switch\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 15:11
-	1115	ccd adj (shift adj register or shift-register)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 15:15
-	268	(ccd adj (shift adj register or shift-register)) and capacitor\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 15:15
-	1	((ccd adj (shift adj register or shift-register)) and capacitor\$2) and cross near3 capacitance	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 15:14
-	197	((ccd adj (shift adj register or shift-register)) and capacitor\$2) and phas\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 15:15
-	77	((ccd adj (shift adj register or shift-register)) and capacitor\$2) and (2-phas\$3 or 348/312 adj phase or two-phase or two adj phase)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/25 15:16
-	2985	clock adj driver	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 11:44
-	41151	(clock adj driver) and ccd or charge adj coupled adj device or charge-coupled	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 11:45
-	404	(clock adj driver) and (ccd or charge adj coupled adj device or charge-coupled)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 13:46

-	156	((clock adj driver) and (ccd or charge adj coupled adj device or charge-coupled)) and transistor\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 13:47
-	123	((((clock adj driver) and (ccd or charge adj coupled adj device or charge-coupled)) and transistor\$2) and ((decrease or reduce) with energy or power)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 11:50
-	122	((((clock adj driver) and (ccd or charge adj coupled adj device or charge-coupled)) and transistor\$2) and ((decrease or reduce) with energy or power)) and voltage	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 11:46
-	87	(((((clock adj driver) and (ccd or charge adj coupled adj device or charge-coupled)) and transistor\$2) and ((decrease or reduce) with energy or power)) and voltage) and switch\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 11:47
-	34	(((((clock adj driver) and (ccd or charge adj coupled adj device or charge-coupled)) and transistor\$2) and ((decrease or reduce) with energy or power)) and voltage) and switch\$2) and buses	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 11:49
-	56086	eastman adj kodak .asn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 11:50
-	1919	(eastman adj kodak .asn.) and ccd	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 11:50
-	1956	(eastman adj kodak .asn.) and ccd\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 11:50
-	177	((eastman adj kodak .asn.) and ccd\$2) and transistor\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 11:50
-	96	((eastman adj kodak .asn.) and ccd\$2) and transistor\$3) and ((decrease or reduce) with energy or power)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 11:50
-	34	(((((eastman adj kodak .asn.) and ccd\$2) and transistor\$3) and ((decrease or reduce) with energy or power)) and bus\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 11:51
-	4	("4354123" "4949183" "5047660" "5237422").PN.	USPAT	2004/10/26 11:57
-	14	("4117514" "4424456" "4514766" "4518978" "4602289" "4638361" "4680636" "4707744" "4716317" "4742395" "4761565" "4866299" "4935794" "5047660").PN.	USPAT	2004/10/26 12:09
-	4	5237422.URPN.	USPAT	2004/10/26 12:57
-	4	("4354123" "4949183" "5047660" "5237422").PN.	USPAT	2004/10/26 12:21
-	69	((clock adj driver) and (ccd or charge adj coupled adj device or charge-coupled)) and transistor\$2) and vertical	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 12:42

-	25	three with (level or voltage) with clock adj driver	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 12:42
-	151	((clock adj driver) and (ccd or charge adj coupled adj device or charge-coupled)) and transistor\$2) and voltage	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 12:57
-	27	((clock adj driver) and (ccd or charge adj coupled adj device or charge-coupled)) and transistor\$2) and voltage with (three or third)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 12:52
-	104	((clock adj driver) and (ccd or charge adj coupled adj device or charge-coupled)) and transistor\$2) and voltage) not (((clock adj driver) and (ccd or charge adj coupled adj device or charge-coupled)) and transistor\$2) and voltage with (three or third))	USPAT	2004/10/26 13:01
-	10	((clock adj driver) and (ccd or charge adj coupled adj device or charge-coupled)) and transistor\$2) and voltage) not (((clock adj driver) and (ccd or charge adj coupled adj device or charge-coupled)) and transistor\$2) and voltage with (three or third))) and clock .ti.	USPAT	2004/10/26 12:58
-	94	((clock adj driver) and (ccd or charge adj coupled adj device or charge-coupled)) and transistor\$2) and voltage) not (((clock adj driver) and (ccd or charge adj coupled adj device or charge-coupled)) and transistor\$2) and voltage with (three or third))) not (((clock adj driver) and (ccd or charge adj coupled adj device or charge-coupled)) and transistor\$2) and voltage) not (((clock adj driver) and (ccd or charge adj coupled adj device or charge-coupled)) and transistor\$2) and voltage with (three or third))) and clock .ti.)	USPAT	2004/10/26 13:01
-	5	"3758794" "3955100" "4093872" "4178519" "4264915").PN.	USPAT	2004/10/26 13:21
-	5	4554675.URPN.	USPAT	2004/10/26 13:23
-	127	charge adj coupled adj shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 13:24
-	73	(charge adj coupled adj shift adj register) and clock	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 13:25
-	60	((charge adj coupled adj shift adj register) and clock) and phas\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 13:25
-	31493	V1 with V2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 13:46

-	1298	(V1 with V2) and (ccd or charge adj coupled adj device or charge-coupled)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 13:46
-	500	((V1 with V2) and (ccd or charge adj coupled adj device or charge-coupled)) and phase\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 13:46
-	261	((V1 with V2) and (ccd or charge adj coupled adj device or charge-coupled)) and phase\$2) and clock	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 13:47
-	100	((V1 with V2) and (ccd or charge adj coupled adj device or charge-coupled)) and phase\$2) and clock) and transistor\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 13:52
-	14	((V1 with V2) and (ccd or charge adj coupled adj device or charge-coupled)) and phase\$2) and clock) and transistor\$2) and clock adj driver	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 13:48
-	60	((V1 with V2) and (ccd or charge adj coupled adj device or charge-coupled)) and phase\$2) and clock) and transistor\$2) and transfer\$5 with charge	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 13:52
-	1	"3902186".PN.	USPAT	2004/10/26 14:07
-	4	4443885.URPN.	USPAT	2004/10/26 14:07